

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6687247 B1	20040203	61	Architecture for high speed class of service enabled linecard	370/392	370/412
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6631134 B1	20031007	29	Method for allocating bandwidth in an optical network	370/395.21	370/252; 370/409; 370/468
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6597689 B1	20030722	81	SVC signaling system and method	370/354	370/395.1; 370/430
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6510166 B2	20030121	17	Stuffing filter mechanism for data transmission signals	370/505	370/506
5	<input type="checkbox"/>	<input type="checkbox"/>	US 5677905 A	19971014	56	Access subnetwork controller for video dial tone networks	370/395.21	725/104; 725/119; 725/129; 725/87; 725/95

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1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6687247 B1	20040203	61	Architecture for high speed class of service enabled linecard	370/392	370/412
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658580 B1	20031202	106	Redundant, synchronous central timing systems with constant master voltage controls and variable slave voltage controls	713/400	331/2; 713/500
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658579 B1	20031202	119	Network device with local timing systems for automatic selection between redundant, synchronous central timing systems	713/400	713/500
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6597689 B1	20030722	81	SVC signaling system and method	370/354	370/395.1; 370/430
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6400291 B1	20020604	10	Multiple time domain serial-to-parallel converter	341/100	341/101
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6226296 B1	20010501	57	Metropolitan area network switching system and method of operation thereof	370/401	370/535; 398/9
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4977582 A	19901211	62	Synchronization of non-continuous digital bit streams	375/371	370/517
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4958341 A	19900918	63	Integrated packetized voice and data switching system	370/352	370/380; 370/427
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4942574 A	19900717	63	Concurrent resource request resolution mechanism	370/400	340/825.5
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4922486 A	19900501	62	User to network interface protocol for packet communications networks	370/427	340/5.74
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4899333 A	19900206	62	Architecture of the control of a high performance packet switching distribution network	370/427	
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4897874 A	19900130	62	Metropolitan area network arrangement for serving virtual data networks	713/201	340/5.74; 370/422; 370/427; 713/155; 713/162
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4896319 A	19900123	62	Identification and authentication of end user systems for packet communications network services	370/427	340/5.8
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4894824 A	19900116	63	Control network for a rapid connection circuit switch	370/380	

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4893302 A	19900109	61	Arrangement for switching concentrated telecommunications packet traffic	370/427	
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4875206 A	19891017	61	High bandwidth interleaved buffer memory and control	370/427	370/460; 370/907
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872160 A	19891003	64	Integrated packetized voice and data switching system	370/353	370/427
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872159 A	19891003		Packet network architecture for providing rapid response time	370/352	370/380
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872158 A	19891003		Distributed control rapid connection circuit switch	370/380	
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872157 A	19891003		Architecture and organization of a high performance metropolitan area telecommunications packet network	370/400	370/425

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6690668 B1	20040210	29	Modular interconnection of network switches	370/392	370/403
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6687247 B1	20040203	61	Architecture for high speed class of service enabled linecard	370/392	370/412
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658580 B1	20031202	106	Redundant, synchronous central timing systems with constant master voltage controls and variable slave voltage controls	713/400	331/2; 713/500
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658579 B1	20031202	119	Network device with local timing systems for automatic selection between redundant, synchronous central timing systems	713/400	713/500
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6650646 B1	20031118	22	Digital communications system	370/397	370/406
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6628657 B1	20030930	41	Method and system for transporting synchronous and asynchronous traffic on a bus of a telecommunications node	370/395.1	370/347
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6621828 B1	20030916	40	Fused switch core and method for a telecommunications node	370/466	370/376; 370/395.1; 370/458
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6621818 B1	20030916	29	Ring configuration for network switches	370/389	370/393
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6611867 B1	20030826	163	System, method and article of manufacture for implementing a hybrid network	709/224	709/218; 709/249
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6611526 B1	20030826	24	System having a meshed backplane and process for transferring data therethrough	370/406	370/369; 370/376; 370/389
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6606300 B1	20030812	65	Flow control process for a switching system and system for performing the same	370/229	370/389
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6597689 B1	20030722	81	SVC signaling system and method	370/354	370/395.1; 370/430
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6570872 B1	20030527	31	Self-configuring distributed switch	370/369	370/374; 398/79
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6483850 B1	20021119	60	Method and apparatus for routing cells having different formats among service modules of a switch platform	370/466	370/467

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6427132 B1	20020730	166	System, method and article of manufacture for demonstrating E-commerce capabilities via a simulation on a network	703/22	703/6; 705/26
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6400291 B1	20020604	10	Multiple time domain serial-to-parallel converter	341/100	341/101
17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6345239 B1	20020205	164	Remote demonstration of business capabilities in an e-commerce environment	703/6	705/26; 705/39
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6343081 B1	20020129		Method and apparatus for managing contention in a self-routing switching architecture in a port expansion mode	370/411	370/462
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6335927 B1	20020101		System and method for providing requested quality of service in a hybrid network	370/352	
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6324164 B1	20011127		Asynchronous transfer mode (A.T.M.) protocol adapter for a high speed cell switching system	370/230	370/235; 370/392; 370/395.65 ; 370/399; 370/429; 370/465; 370/474
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6226296 B1	20010501	57	Metropolitan area network switching system and method of operation thereof	370/401	370/535; 398/9
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6125114 A	20000926		Switching system comprising distributed elements allowing attachment to line adapters, and having multicasting capabilities	370/389	370/413
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6108334 A	20000822		Switching system comprising distributed elements allowing attachment to line adapters	370/389	370/429
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6055235 A	20000425		Switching system	370/389	370/429
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5999525 A	19991207		Method for video telephony over a hybrid network	370/352	370/389; 370/392; 379/114.15 ; 379/90.01; 379/93.07

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26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5933607 A	19990803		Digital communication system for simultaneous transmission of data from constant and variable rate sources	370/395.61	370/468
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5867494 A	19990202		System, method and article of manufacture with integrated video conferencing billing in a communication system architecture	370/352	370/389; 370/392; 379/114.15; 379/90.01; 379/93.07
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5818842 A	19981006		Transparent interconnector of LANs by an ATM network	370/397	370/250; 370/412; 370/419; 370/465; 370/474
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5790546 A	19980804		Method of transmitting data packets in a packet switched communications network	370/400	370/392
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5689508 A	19971118		Reservation ring mechanism for providing fair queued access in a fast packet switch networks	370/391	370/418; 370/424
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5634015 A	19970527		Generic high bandwidth adapter providing data communications between diverse communication networks and computer system	710/310	370/402; 370/412; 710/62
32	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5521910 A	19960528		Method for determining a best path between two nodes	370/256	370/406
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5519698 A	19960521		Modification to a reservation ring mechanism for controlling contention in a broadband ISDN fast packet switch suitable for use in a local area network	370/411	370/414
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5491694 A	19960213		System and method for allocating a shared resource among competing devices	370/455	370/461
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5485455 A	19960116		Network having secure fast packet switching and guaranteed quality of service	370/255	370/400; 370/409

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
36	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5367643 A	19941122		Generic high bandwidth adapter having data packet memory configured in three level hierarchy for temporary storage of variable length data packets	710/62	370/412; 709/231
37	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4977582 A	19901211	62	Synchronization of non-continuous digital bit streams	375/371	370/517
38	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4958341 A	19900918	63	Integrated packetized voice and data switching system	370/352	370/380; 370/427
39	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4942574 A	19900717	63	Concurrent resource request resolution mechanism	370/400	340/825.5
40	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4922486 A	19900501	62	User to network interface protocol for packet communications networks	370/427	340/5.74
41	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4899333 A	19900206	62	Architecture of the control of a high performance packet switching distribution network	370/427	
42	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4897874 A	19900130	62	Metropolitan area network arrangement for serving virtual data networks	713/201	340/5.74; 370/422; 370/427; 713/155; 713/162
43	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4896319 A	19900123	62	Identification and authentication of end user systems for packet communications network services	370/427	340/5.8
44	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4894824 A	19900116	63	Control network for a rapid connection circuit switch	370/380	
45	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4893302 A	19900109	61	Arrangement for switching concentrated telecommunications packet traffic	370/427	
46	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4875206 A	19891017	61	High bandwidth interleaved buffer memory and control	370/427	370/460; 370/907
47	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872160 A	19891003	64	Integrated packetized voice and data switching system	370/353	370/427
48	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872159 A	19891003		Packet network architecture for providing rapid response time	370/352	370/380
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872158 A	19891003		Distributed control rapid connection circuit switch	370/380	

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50	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872157 A	19891003		Architecture and organization of a high performance metropolitan area telecommunications packet network	370/400	370/425
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4720850 A	19880119		Communication system control arrangement	379/90.01	370/259; 379/230; 379/269; 379/284; 902/39
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4713806 A	19871215		Communication system control arrangement	370/358	902/39

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1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6694450 B1	20040217	77	Distributed process redundancy	714/15	707/204; 714/13
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6690668 B1	20040210	29	Modular interconnection of network switches	370/392	370/403
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6687247 B1	20040203	61	Architecture for high speed class of service enabled linecard	370/392	370/412
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6674754 B1	20040106	77	Wavelength division multiplexing combined with time division multiplexing using a common time reference	370/389	398/51; 709/248
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6671699 B1	20031230	76	Shared database usage in network devices	707/201	707/200
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6671275 B1	20031230	18	Cross-point switch with deadlock prevention	370/389	370/428; 370/429
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6667984 B1	20031223	61	Methods and apparatus for arbitrating output port contention in a switch having virtual output queuing	370/414	
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6661788 B2	20031209	31	Multicast scheduling for a network device	370/390	370/413; 370/416; 370/432
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658580 B1	20031202	106	Redundant, synchronous central timing systems with constant master voltage controls and variable slave voltage controls	713/400	331/2; 713/500
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6658579 B1	20031202	119	Network device with local timing systems for automatic selection between redundant, synchronous central timing systems	713/400	713/500
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6654903 B1	20031125	76	Vertical fault isolation in a computer system	714/4	714/43
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6650646 B1	20031118	22	Digital communications system	370/397	370/406
13	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6647019 B1	20031111	20	Packet-switch system	370/422	370/229; 370/395.1
14	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6628657 B1	20030930	41	Method and system for transporting synchronous and asynchronous traffic on a bus of a telecommunications node	370/395.1	370/347
15	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6628646 B1	20030930		Programmable multicast scheduling for a network device	370/355	370/429; 370/432
16	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6621828 B1	20030916	40	Fused switch core and method for a telecommunications node	370/466	370/376; 370/395.1; 370/458

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17	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6621818 B1	20030916	29	Ring configuration for network switches	370/389	370/393
18	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6611867 B1	20030826	163	System, method and article of manufacture for implementing a hybrid network	709/224	709/218; 709/249
19	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6611526 B1	20030826	24	System having a meshed backplane and process for transferring data therethrough	370/406	370/369; 370/376; 370/389
20	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6606300 B1	20030812	65	Flow control process for a switching system and system for performing the same	370/229	370/389
21	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6601186 B1	20030729		Independent restoration of control plane and data plane functions	714/4	714/44; 719/321; 719/327
22	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6597689 B1	20030722	81	SVC signaling system and method	370/354	370/395.1; 370/430
23	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6570872 B1	20030527	31	Self-configuring distributed switch	370/369	370/374; 398/79
24	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6519225 B1	20030211		Backpressure mechanism for a network device	370/229	370/389; 370/390
25	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6487213 B1	20021126		Methods and apparatus for fairly arbitrating contention for an output port	370/418	370/428
26	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6483850 B1	20021119	60	Method and apparatus for routing cells having different formats among service modules of a switch platform	370/466	370/467
27	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6477169 B1	20021105		Multicast and unicast scheduling for a network device	370/395.42	370/395.4; 370/395.41; 370/395.43
28	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6449283 B1	20020910		Methods and apparatus for providing a fast ring reservation arbitration	370/461	370/445; 370/447; 370/450; 370/462
29	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6442135 B1	20020827		Monitoring, policing and billing for packet switching with a common time reference	370/229	370/395.1; 370/401
30	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6427132 B1	20020730	166	System, method and article of manufacture for demonstrating E-commerce capabilities via a simulation on a network	703/22	703/6; 705/26
31	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6400291 B1	20020604	10	Multiple time domain serial-to-parallel converter	341/100	341/101

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
32	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6377579 B1	20020423		Interconnecting a synchronous switching network that utilizes a common time reference with an asynchronous switching network	370/395.4	
33	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6345239 B1	20020205	164	Remote demonstration of business capabilities in an e-commerce environment	703/6	705/26; 705/39
34	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6343081 B1	20020129		Method and apparatus for managing contention in a self-routing switching architecture in a port expansion mode	370/411	370/462
35	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6335927 B1	20020101		System and method for providing requested quality of service in a hybrid network	370/352	
36	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6332198 B1	20011218		Network device for supporting multiple redundancy schemes	714/6	370/217; 370/220; 714/41
37	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6330236 B1	20011211		Packet switching method with time-based routing	370/369	370/375; 370/376
38	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6324164 B1	20011127		Asynchronous transfer mode (A.T.M.) protocol adapter for a high speed cell switching system	370/230	370/235; 370/392; 370/395.65; ; 370/399; 370/429; 370/465; 370/474
39	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6272132 B1	20010807		Asynchronous packet switching with common time reference	370/389	370/392
40	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6272131 B1	20010807		Integrated data packet network using a common time reference	370/389	370/517
41	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6226296 B1	20010501	57	Metropolitan area network switching system and method of operation thereof	370/401	370/535; 398/9
42	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6125114 A	20000926		Switching system comprising distributed elements allowing attachment to line adapters, and having multicasting capabilities	370/389	370/413
43	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6108334 A	20000822		Switching system comprising distributed elements allowing attachment to line adapters	370/389	370/429

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44	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6055235 A	20000425		Switching system	370/389	370/429
45	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6038230 A	20000314		Packet switching with common time reference over links with dynamically varying delays	370/389	370/517
46	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6032218 A	20000229		Configurable weighted round robin arbiter	710/240	710/111; 710/243
47	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5999525 A	19991207		Method for video telephony over a hybrid network	370/352	370/389; 370/392; 379/114.15; 379/90.01; 379/93.07
48	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5933607 A	19990803		Digital communication system for simultaneous transmission of data from constant and variable rate sources	370/395.61	370/468
49	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5923656 A	19990713		Scalable broad band input-queued ATM switch including weight driven cell scheduler	370/395.4	370/412; 370/416
50	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5867494 A	19990202		System, method and article of manufacture with integrated video conferencing billing in a communication system architecture	370/352	370/389; 370/392; 379/114.15; 379/90.01; 379/93.07
51	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5818842 A	19981006		Transparent interconnector of LANs by an ATM network	370/397	370/250; 370/412; 370/419; 370/465; 370/474
52	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5790546 A	19980804		Method of transmitting data packets in a packet switched communications network	370/400	370/392
53	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5724351 A	19980303		Scaleable multicast ATM switch	370/395.42	370/389; 370/397; 370/905
54	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5689508 A	19971118		Reservation ring mechanism for providing fair queued access in a fast packet switch networks	370/391	370/418; 370/424
55	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5634015 A	19970527		Generic high bandwidth adapter providing data communications between diverse communication networks and computer system	710/310	370/402; 370/412; 710/62
56	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5526352 A	19960611		Integrated low complexity broadband multi-channel switch	370/395.4	370/411

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57	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5521910 A	19960528		Method for determining a best path between two nodes	370/256	370/406
58	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5519698 A	19960521		Modification to a reservation ring mechanism for controlling contention in a broadband ISDN fast packet switch suitable for use in a local area network	370/411	370/414
59	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5511070 A	19960423		Reservation ring mechanism for controlling contention in a broadband ISDN fast packet switch suitable for use in a local area network	370/411	370/414
60	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5491694 A	19960213		System and method for allocating a shared resource among competing devices	370/455	370/461
61	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5485455 A	19960116		Network having secure fast packet switching and guaranteed quality of service	370/255	370/400; 370/409
62	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5440549 A	19950808		Broadband multi-channel switch with multicasting capability	370/394	370/395.1; 370/411
63	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5367643 A	19941122		Generic high bandwidth adapter having data packet memory configured in three level hierarchy for temporary storage of variable length data packets	710/62	370/412; 709/231
64	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5327420 A	19940705		Method for building multi-bit parallel Batcher/banyan networks	370/397	370/411
65	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5325356 A	19940628		Method for aggregating ports on an ATM switch for the purpose of trunk grouping	370/397	370/411
66	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5305311 A	19940419		Copy network providing multicast capabilities in a broadband ISDN fast packet switch suitable for use in a local area network	370/390	370/397
67	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5303078 A	19940412		Apparatus and method for large scale ATM switching	398/51	370/395.31 ; 370/399; 398/50; 398/54

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68	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4977582 A	19901211	62	Synchronization of non-continuous digital bit streams	375/371	370/517
69	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4958341 A	19900918	63	Integrated packetized voice and data switching system	370/352	370/380; 370/427
70	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4942574 A	19900717	63	Concurrent resource request resolution mechanism	370/400	340/825.5
71	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4922486 A	19900501	62	User to network interface protocol for packet communications networks	370/427	340/5.74
72	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4899333 A	19900206	62	Architecture of the control of a high performance packet switching distribution network	370/427	
73	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4897874 A	19900130	62	Metropolitan area network arrangement for serving virtual data networks	713/201	340/5.74; 370/422; 370/427; 713/155; 713/162
74	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4896319 A	19900123	62	Identification and authentication of end user systems for packet communications network services	370/427	340/5.8
75	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4894824 A	19900116	63	Control network for a rapid connection circuit switch	370/380	
76	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4893302 A	19900109	61	Arrangement for switching concentrated telecommunications packet traffic	370/427	
77	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4875206 A	19891017	61	High bandwidth interleaved buffer memory and control	370/427	370/460; 370/907
78	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872160 A	19891003	64	Integrated packetized voice and data switching system	370/353	370/427
79	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872159 A	19891003		Packet network architecture for providing rapid response time	370/352	370/380
80	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872158 A	19891003		Distributed control rapid connection circuit switch	370/380	
81	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4872157 A	19891003		Architecture and organization of a high performance metropolitan area telecommunications packet network	370/400	370/425

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82	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4720850 A	19880119		Communication system control arrangement	379/90.01	370/259; 379/230; 379/269; 379/284; 902/39
83	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4713806 A	19871215		Communication system control arrangement	370/358	902/39